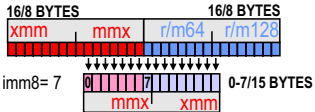
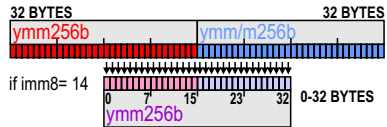


PRENOSNÉ INŠTRUKCIE

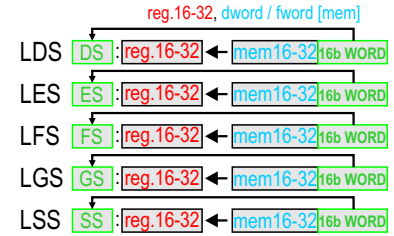
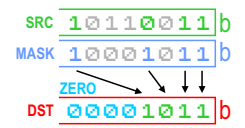
PALIGNR $mmx/xmm, r/m64-128, imm8$



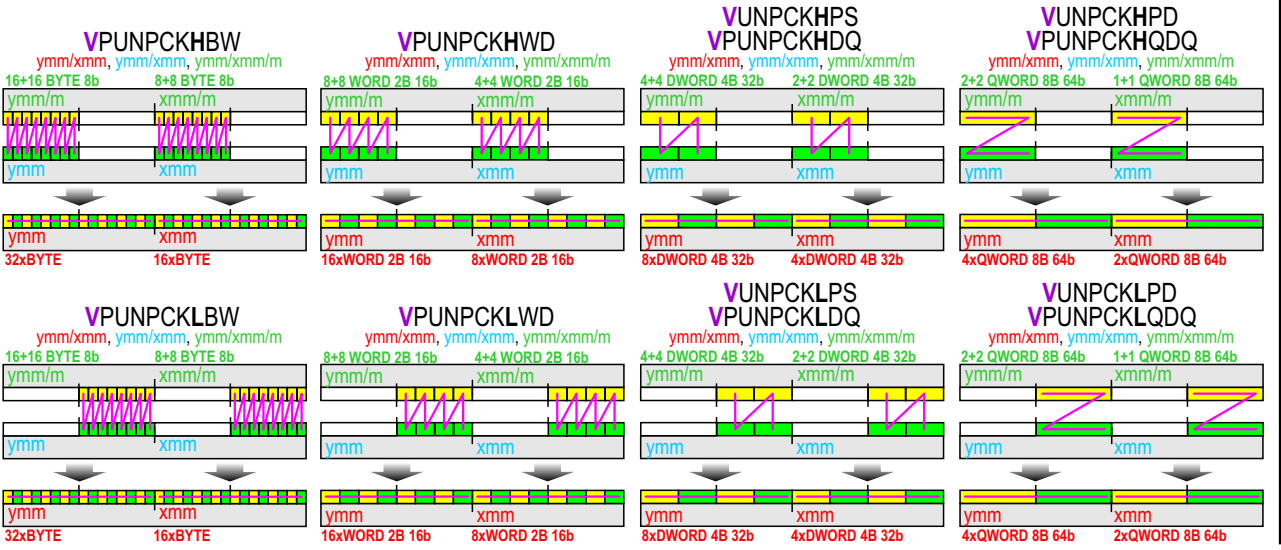
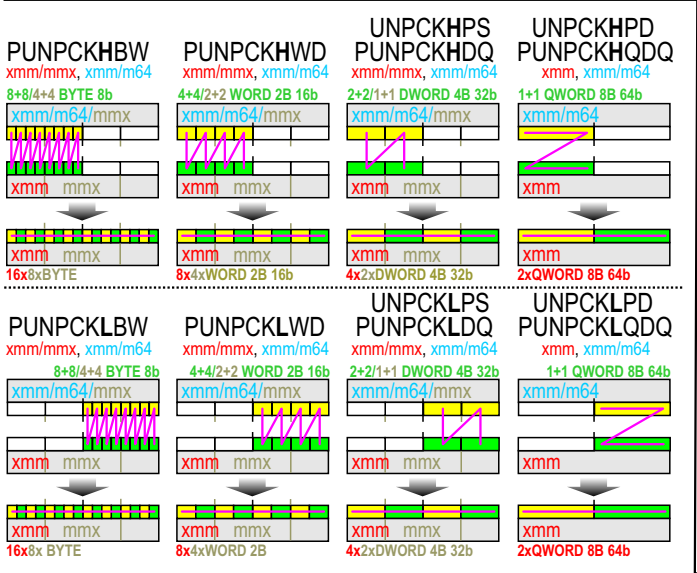
VPALIGNR $ymm, ymm, ymm/m, imm8$



PEXT $DST, SRC, MASK, r32/64, r32/64, r/m32/64$



SHORT skok od -128 až +127B (E)IP+1B
 NEAR v rámci segmentu (E)IP
 FAR mimo hranice segmentu CS:(E)IP



LEAVE
 DWORD 32b/QWORD 64b
 MOV [ESP/RSP] ← [EBP/RBP]
 POP EBP/RBP

ENTER imm, imm
 example: 12, 0
 PUSH EBP/RBP
 MOV [EBP/RBP] ← [ESP/RSP]
 SUB [ESP/RSP], 12

RDTSC PROCESSOR TIME STAMP
 RDX EDX 32b RAX EAX 32b

seconds = $\frac{\text{cycles}}{\text{freq. Hz}}$

RDRAND $r16-64$ ← RANDOM NUMBER
 CF CARRY FLAG=1 NUMBER IS VALID

